

Configuring 10-bit dsPIC30F A/D Converters for 1 Msp Conversion Rate

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INTRODUCTION

Several dsPIC30F devices feature 10-bit A/D converters that are capable of converting up to 1 Mega samples per second (Mps). This capability allows applications to sample high-frequency analog signals. This code example demonstrates how the application can configure the A/D converter on the dsPIC[®] DSC to convert signals at such high speeds.

This document and the accompanying code example are intended to supplement **Section 17 “10-Bit A/D Converter”** of the “*dsPIC30F Family Reference Manual*” (DS70046).

THEORY OF OPERATION

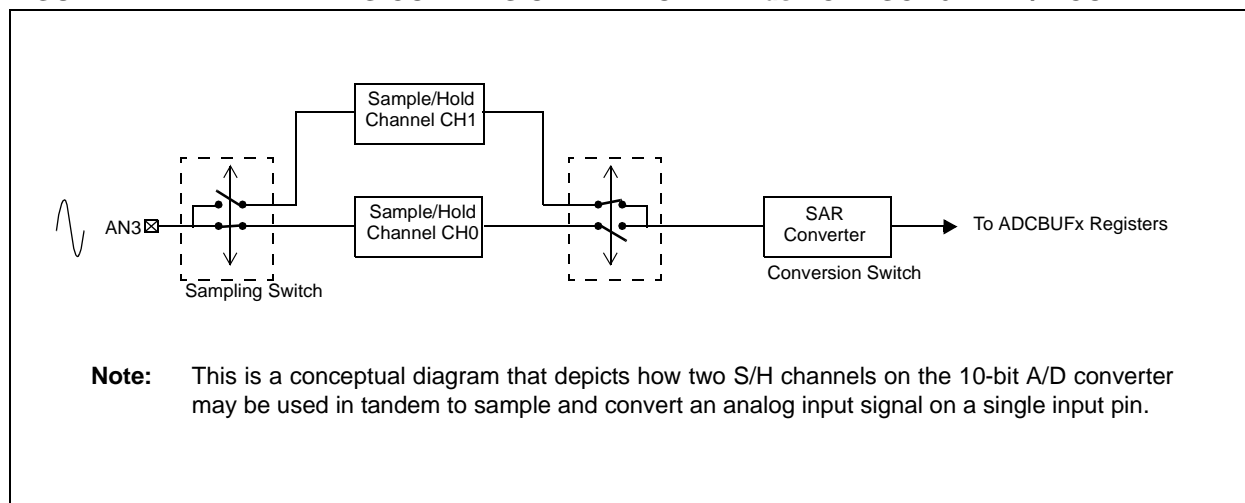
The 10-bit A/D converters on the dsPIC DSC devices contain four sample-and-hold (S/H) channels, namely, CH0, CH1, CH2 and CH3. Analog input pins on the dsPIC DSC are connected to these S/H channels via a system of multiplexers. Typically, any of the analog input pins on the dsPIC DSC can be configured as an input to S/H channel, CH0. S/H channels, CH1, CH2

and CH3, derive their input from one of two choices of analog input pins. The exact pin choices depends on the dsPIC DSC device being used. Special Function Register (SFR) ADCHS is used to set up the analog input pin to be used by each S/H channel. The CHPS (ADCON2<9:8>) bits are used to enable one, two or all four S/H channels. For example, a value of ‘01’ loaded into the CHPS bits enables two S/H channels, CH0 and CH1.

The 10-bit A/D converter can be set up so that two or four S/H channels acquire the analog signals on their respective input pins simultaneously (at the same instant in time) or sequentially (one after the other). The sequential sampling mode allows the 10-bit A/D converter to operate at the maximum conversion rate. The sequential sampling option is set up by clearing the SIMSAM (ADCON1<3>) bit.

To achieve a 1 Msp conversion rate, the application should set up the A/D converter module (via software initialization) to use the same analog input pin for both the S/H channels, CH0 and CH1, as shown in Figure 1. In this scenario, the use of two S/H channels to convert in sequential mode ensures maximum conversion rate. Following is a detailed procedure to configure the module for a 1 Msp conversion rate.

FIGURE 1: MAXIMIZING CONVERSION RATE ON THE dsPIC[®] DSC 10-BIT A/D CONVERTER



PROCEDURE:

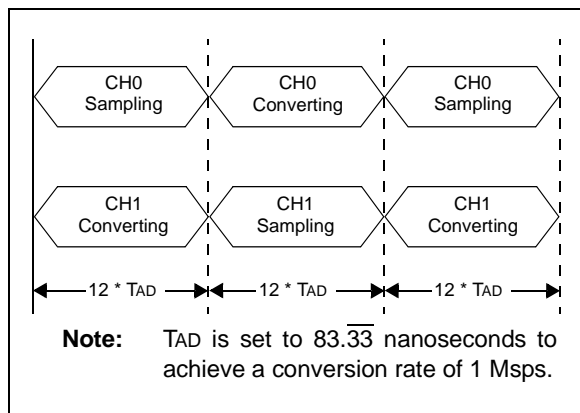
To set up the A/D Converter module to operate at 1 Msps conversion rate, follow these steps in your code:

1. Enable Auto Sampling Mode:
`ASAM(ADCON1<2>) = '1'`
2. Enable Auto Convert Mode:
`SSRC(ADCON1<7:5>) = '111'`
3. Enable Sequential Sampling:
`SIMSAM(ADCON1<3>) = '0'`
4. Enable 2 S/H channels:
`CHPS<1:0>(ADCON2<x:y>) = '01'`
5. Ensure Samples per Interrupt is greater than 1:
`SMPI<3:0>(ADCON2<5:2>) > '0000'`
This ensures that at least one conversion result from each S/H channel is written to the A/D result buffer registers.
6. Select Minimum Sampling Time:
`SAMC<4:0>(ADCON3<12:8>) = '00010'`
7. Select Minimum TAD time: Set up
`ADCS<5:0>(ADCON3<5:0>)`
so that TAD > 83.3 ns
8. Set up ADCHS register so that the analog input pin to CH0 and CH1 S/H channel is the same.
For example, AN3 may be supplied to both CH0 and CH1.

Note: Step 5 is important because the A/D converter writes conversion results starting at location ADCBUF0 on each interrupt. If you use multiple S/H channels in sequential sampling mode, you must configure the SMPI bits so that more than one sample is written to the ADCBUFx registers. This ensures that results from both S/H channels are written to the buffer.

Figure 2 shows a conceptual timing diagram for the 10-bit A/D converters when it is configured for 1 Msps conversion rate using the procedure outlined above.

FIGURE 2: SEQUENTIAL SAMPLING USING 2 S/H CHANNELS



DEVELOPMENT TOOLS

The example workspace was created using MPLAB® IDE v7.11. All source code in the project is written in C. Source-level comments have been provided to aid understanding. The example MPLAB IDE workspace is configured for a dsPIC30F2010 device, but it is easily re-configured for any dsPIC30F device with a 10-bit converter. The analog input pin, AN7, used in this project is available for use on all dsPIC DSC development boards and many dsPIC DSC devices.

The project/workspace assumes that a 7.37 MHz crystal provides device clocking. Such crystals are provided on several dsPIC30F development boards, for example dsPICDEM™ 64-Pin Starter Development Board, dsPICDEM 1.1, dsPICDEM 2, dsPICDEM.net™ and dsPICDEM MC1 Development Boards. Additional oscillator and PLL options have been configured in source code to operate the device at a throughput of 29.4 MIPS. All Microchip software tools and dsPIC30F documentation described in this document can be downloaded from:

<http://www.microchip.com>

SUMMARY

This example project lets you set up the dsPIC30F 10-bit A/D converter for sampling an analog input signal at 1 MHz in an interrupt-driven fashion using C language. Refer to the Readme.txt file for a summary description of the various files and folders provided in the project. Operational aspects are described in source-level comments in each file.

For connection considerations in your circuit, refer to **Section 17 “10-Bit A/D Converter”** in the *“dsPIC30F Family Reference Manual”* (DS70064D, or later).

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